

# **The Ubiquitous Chip**

or Solid State Electronics

- **Electrical - Electricity is the power source**
- **Electronic - Electricity carries information**

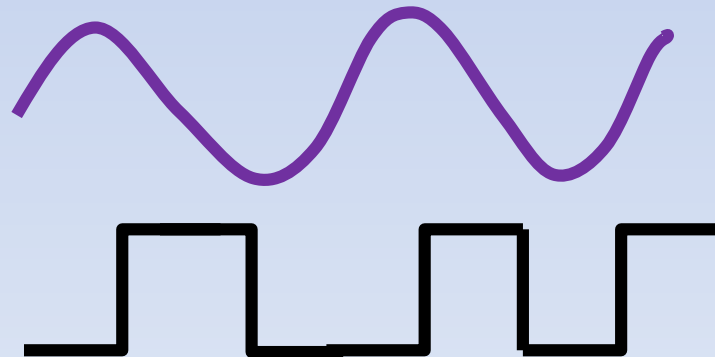
- **Electrical** - Electricity is the power source
- **Electronic** - Electricity carries information
  
- **Voltage** - Electrical Pressure
- **Current** - Electrical charge flow rate

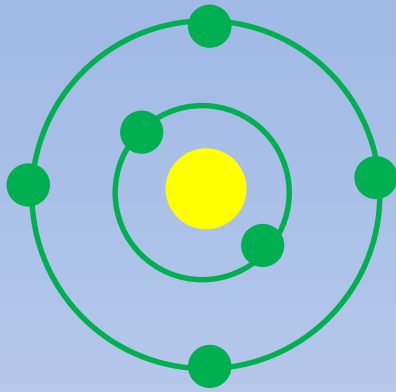
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- **Signal** - An information stream

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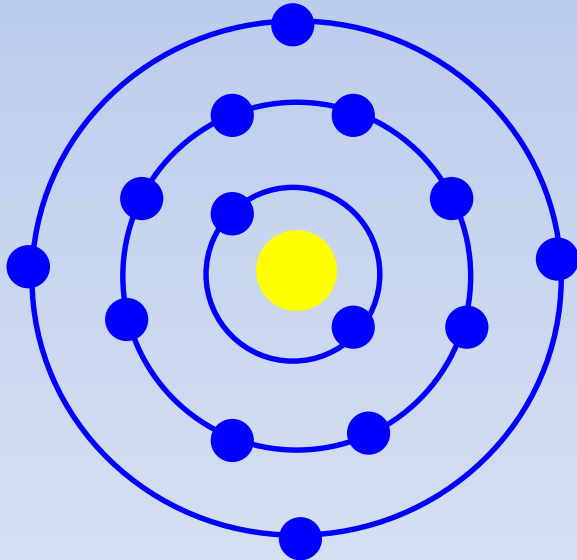
- **Analogue**

- **Digital**

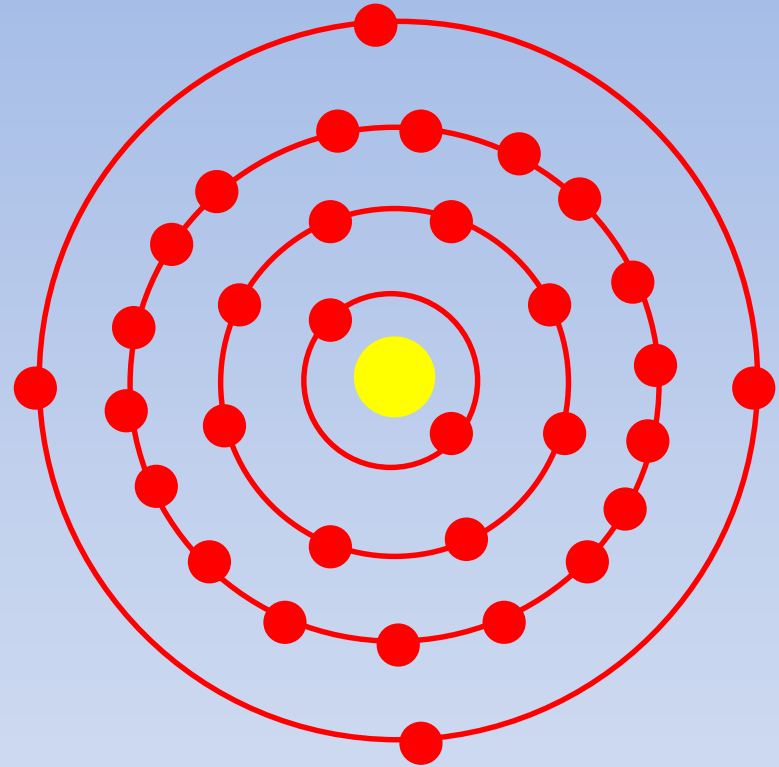




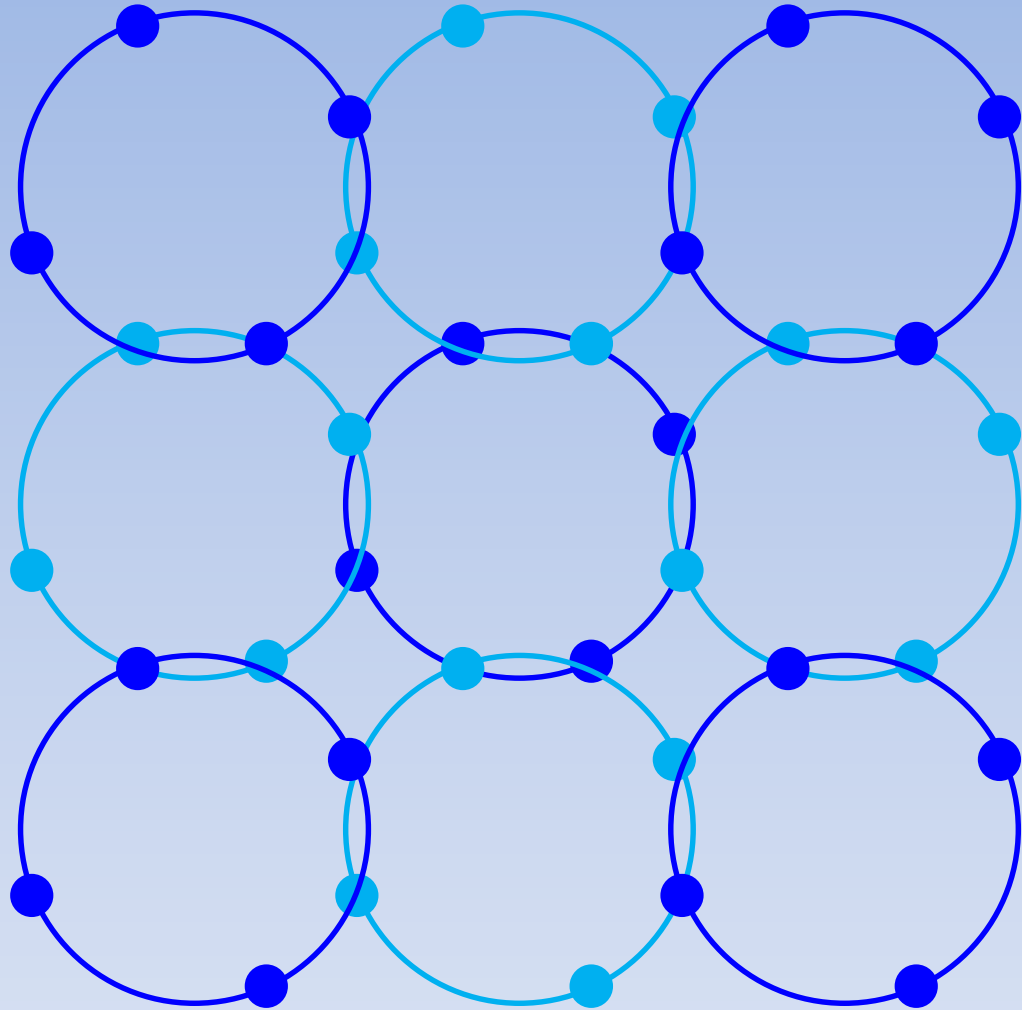
**Carbon**



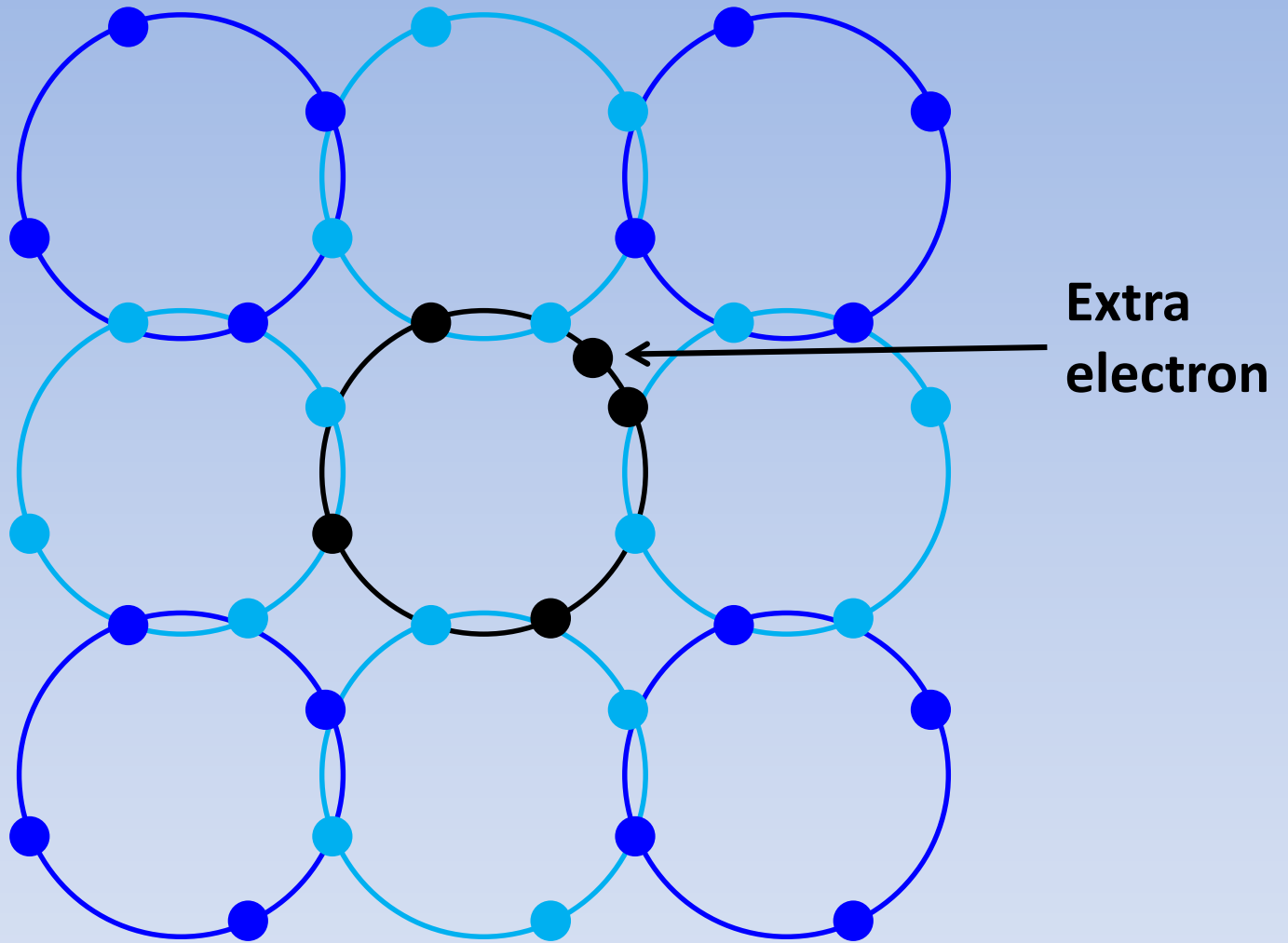
**Silicon**



**Germanium**

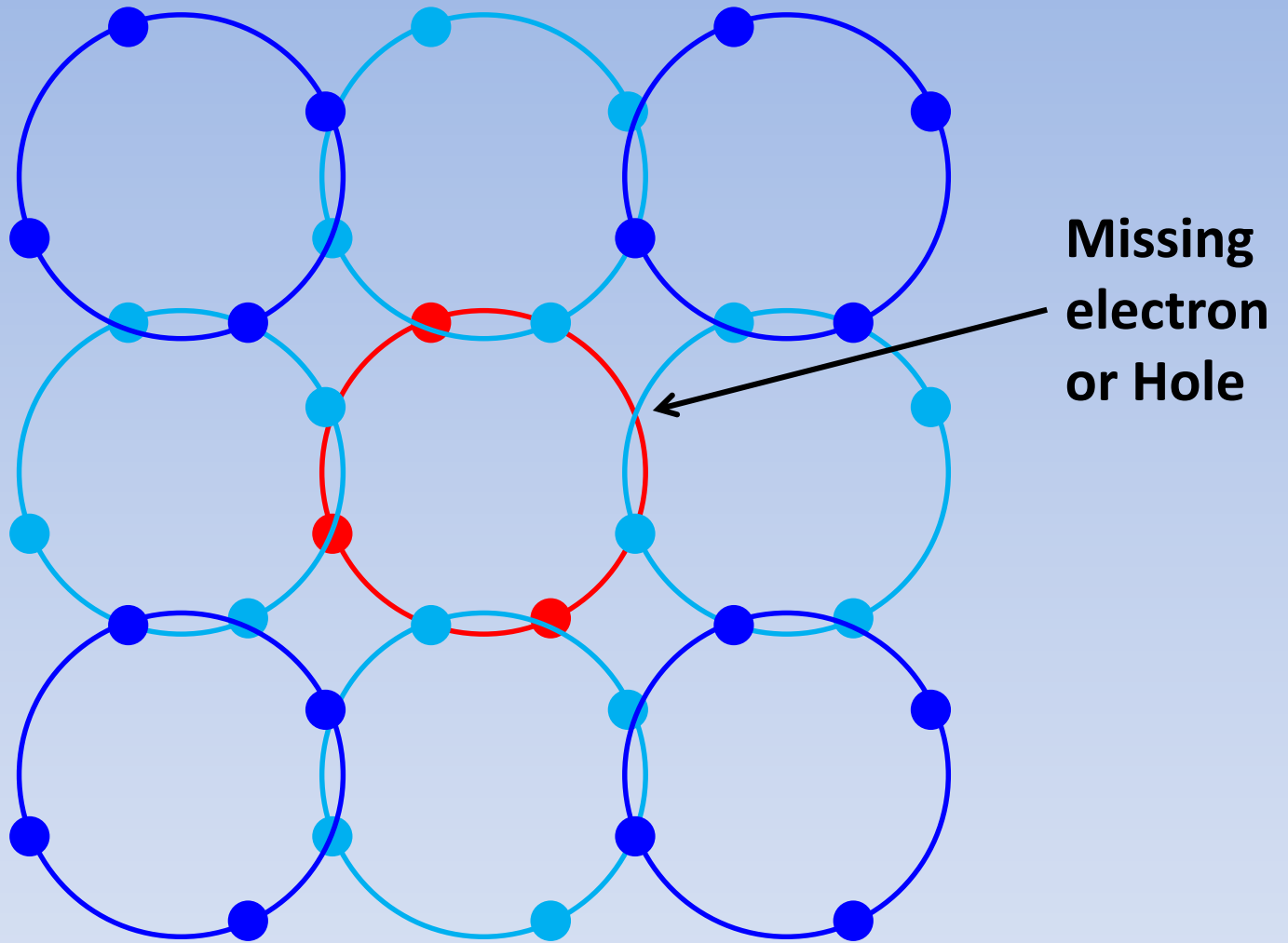


**2-D Version of a Silicon Crystal**

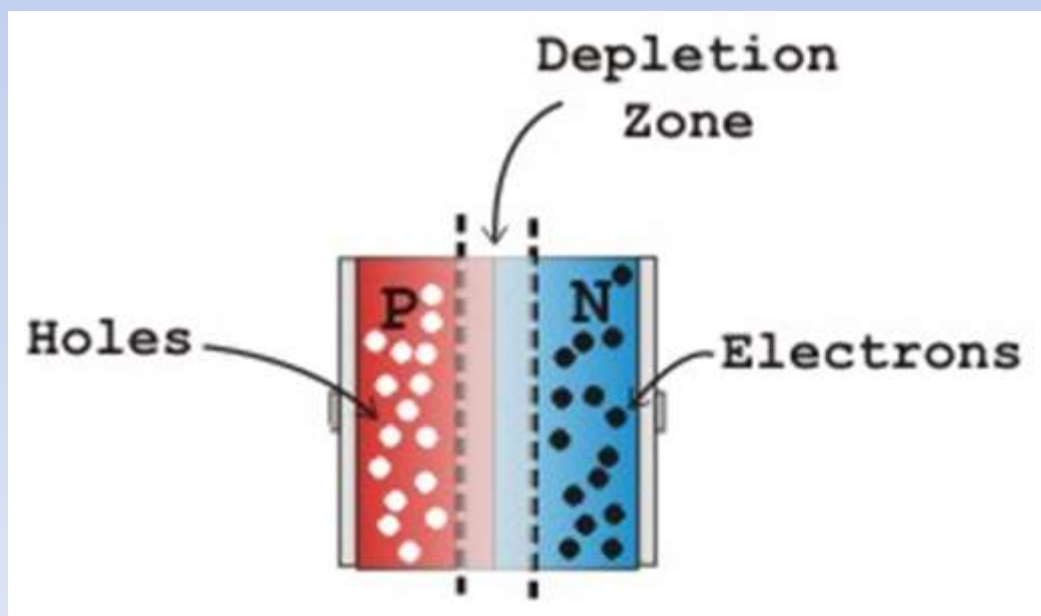
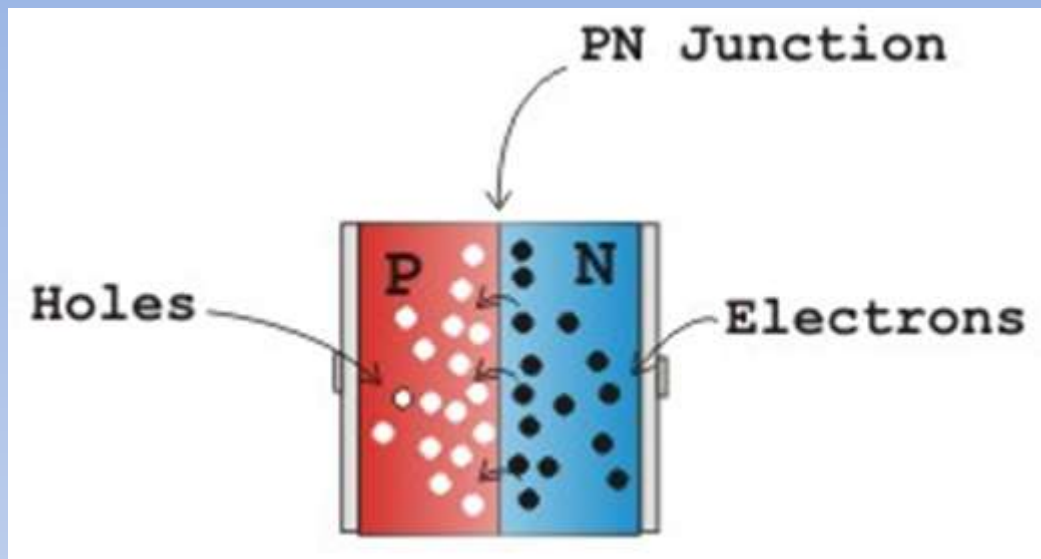


**n-type silicon**

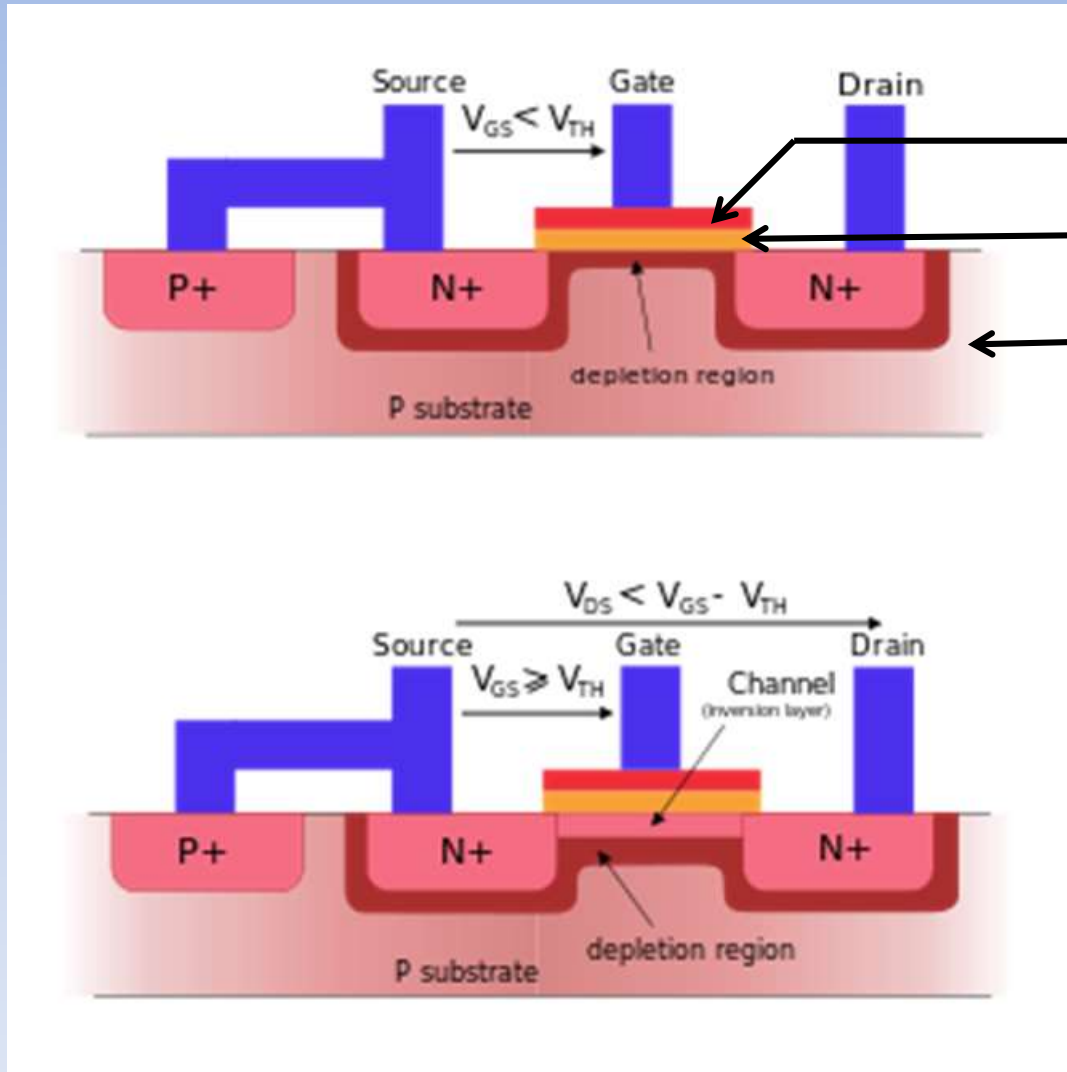




**p-type silicon**



# The MOSFET - Metal Oxide Semiconductor Field Effect Transistor

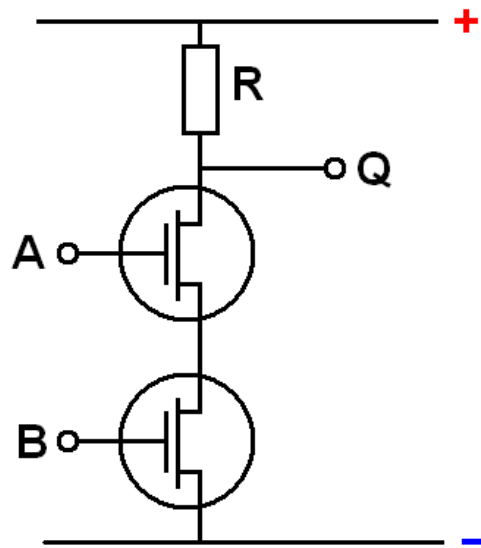


Metal

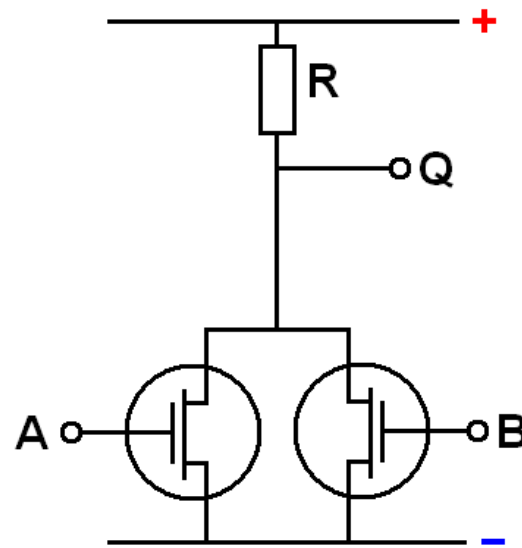
Oxide

Semiconductor

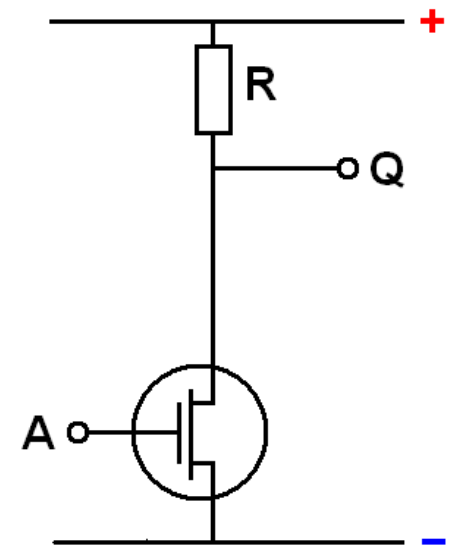
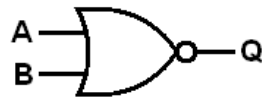
# Logic Circuits ( or Gates )



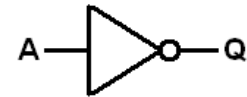
$Q = A \text{ NAND } B$



$Q = A \text{ NOR } B$



$Q = \text{NOT } A$

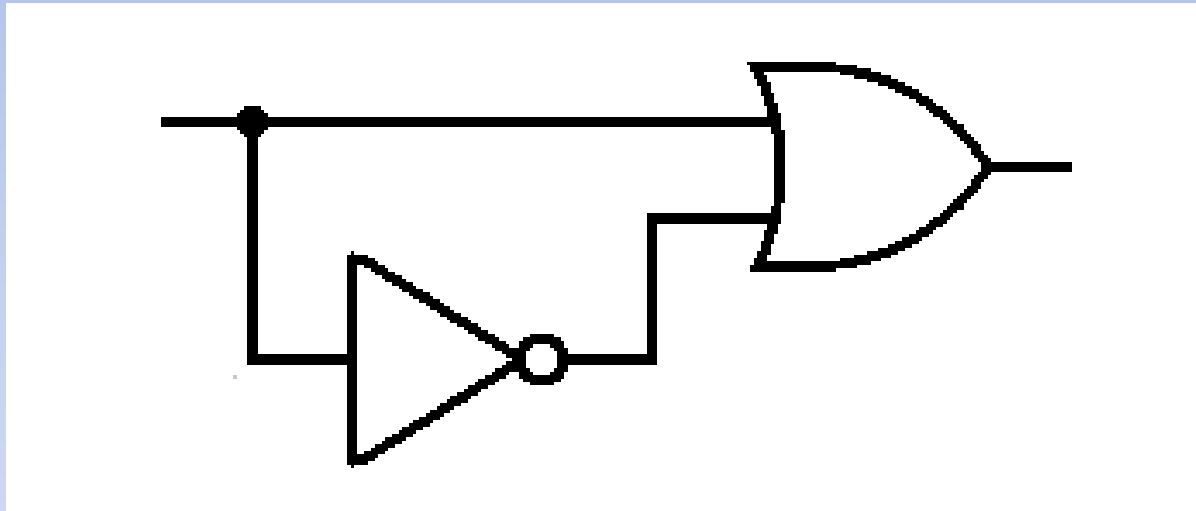


$0 \text{ NAND } 0 = 1$   
 $0 \text{ NAND } 1 = 1$   
 $1 \text{ NAND } 0 = 1$   
 $1 \text{ NAND } 1 = 0$

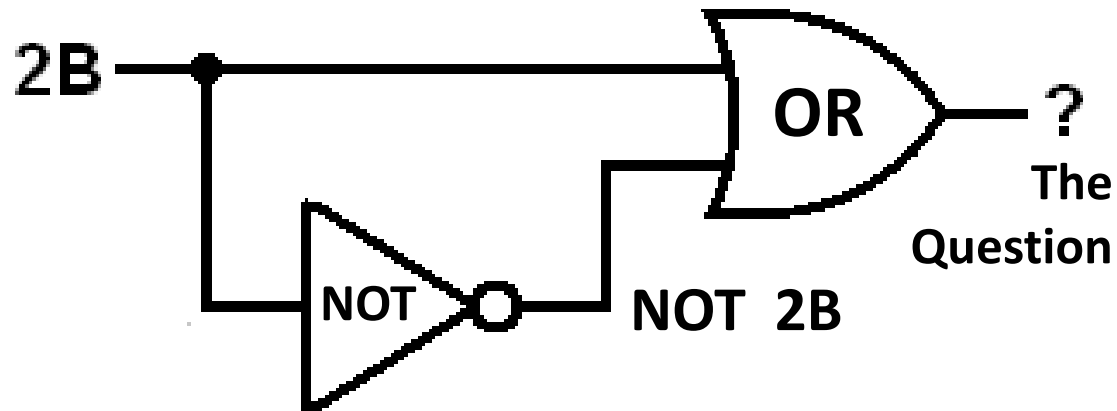
$0 \text{ NOR } 0 = 0$   
 $0 \text{ NOR } 1 = 1$   
 $1 \text{ NOR } 0 = 1$   
 $1 \text{ NOR } 1 = 0$

$\text{NOT } 0 = 1$   
 $\text{NOT } 1 = 0$

# The Shakespeare Circuit



# The Shakespeare Circuit



# The Binary System

**1 x 64**  
**1 x 32**  
**0 x 16**  
**0 x 8**  
**0 x 4**  
**1 x 2**  
**1 x 1 +**

---

**99 decimal = 1100011 binary**

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**1 x 32**  
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**99 decimal = 1100011 binary**

**5**      **101**  
**6 +**      **110 +**  

---

**11**      **1011**



# The Binary System

$$\begin{array}{r} 5 \\ 6 + \\ \hline 11 \end{array} \qquad \begin{array}{r} 101 \\ 110 + \\ \hline 1011 \end{array}$$

**Exclusive OR**

$$\begin{array}{l} 0 \text{ XOR } 0 = 0 \\ 0 \text{ XOR } 1 = 1 \\ 1 \text{ XOR } 0 = 1 \\ 1 \text{ XOR } 1 = 0 \end{array}$$

# The Binary System

$$0 \text{ OR } 0 = 0$$

$$0 \text{ OR } 1 = 1$$

$$1 \text{ OR } 0 = 1$$

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**OR**

**Exclusive OR**

$$5 \qquad 101$$

$$\underline{6} + \qquad \underline{110} +$$

$$11 \qquad 1011$$

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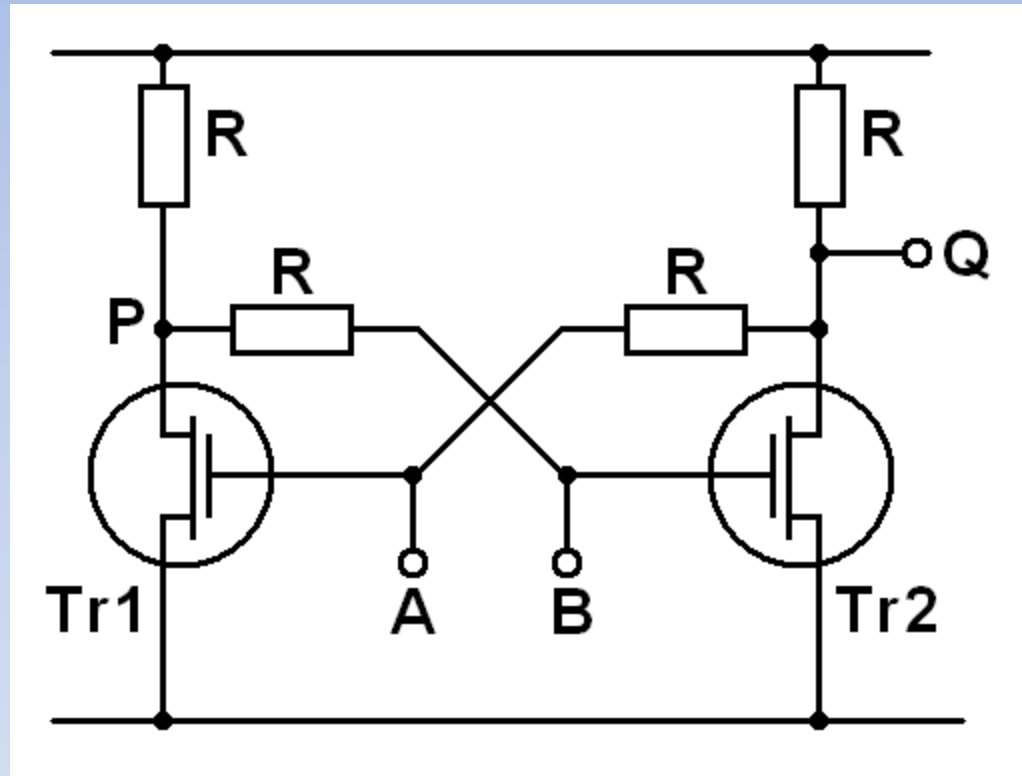
$$0 \text{ XOR } 0 = 0$$

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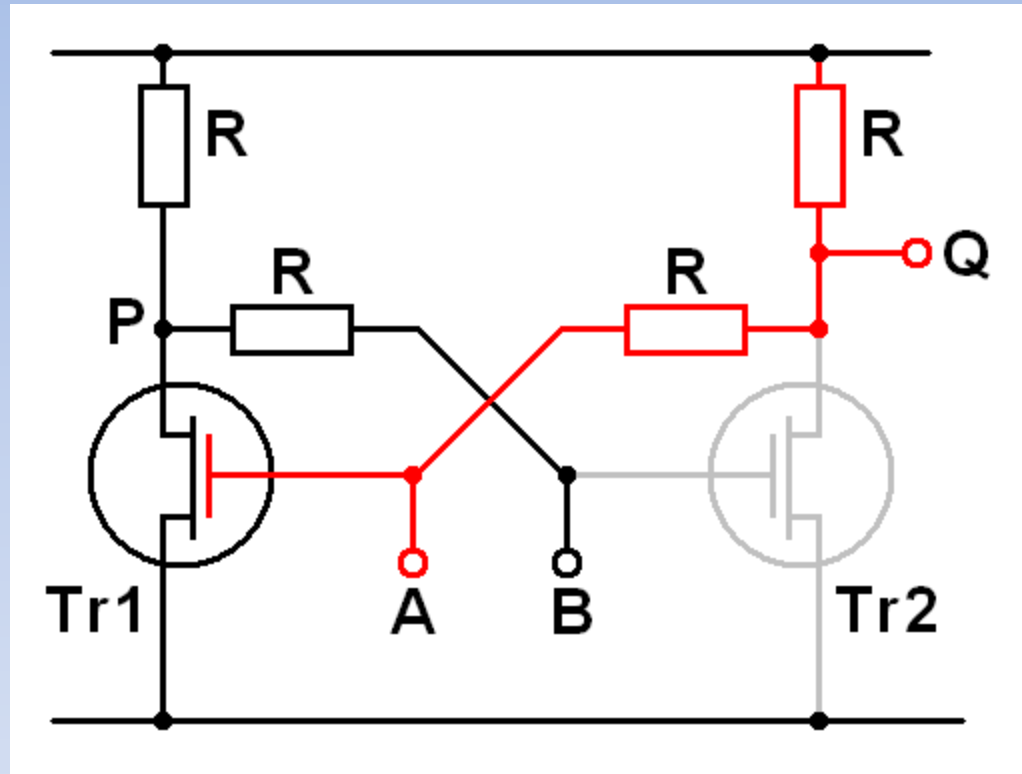
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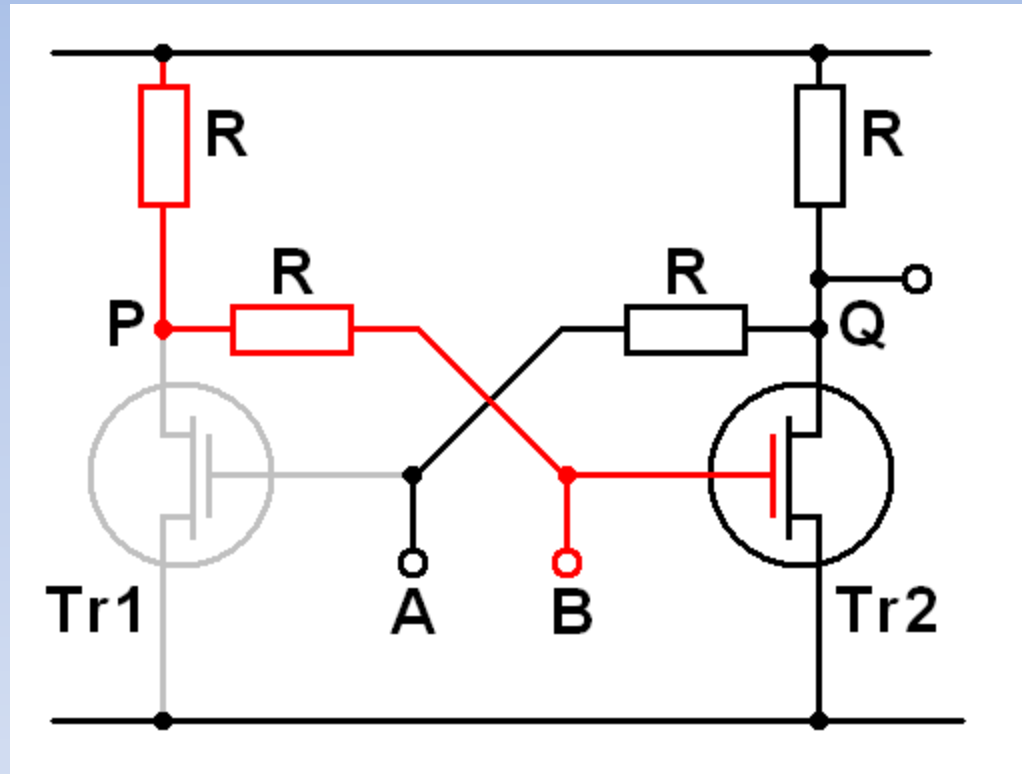
# The Flip-Flop - A Memory Circuit



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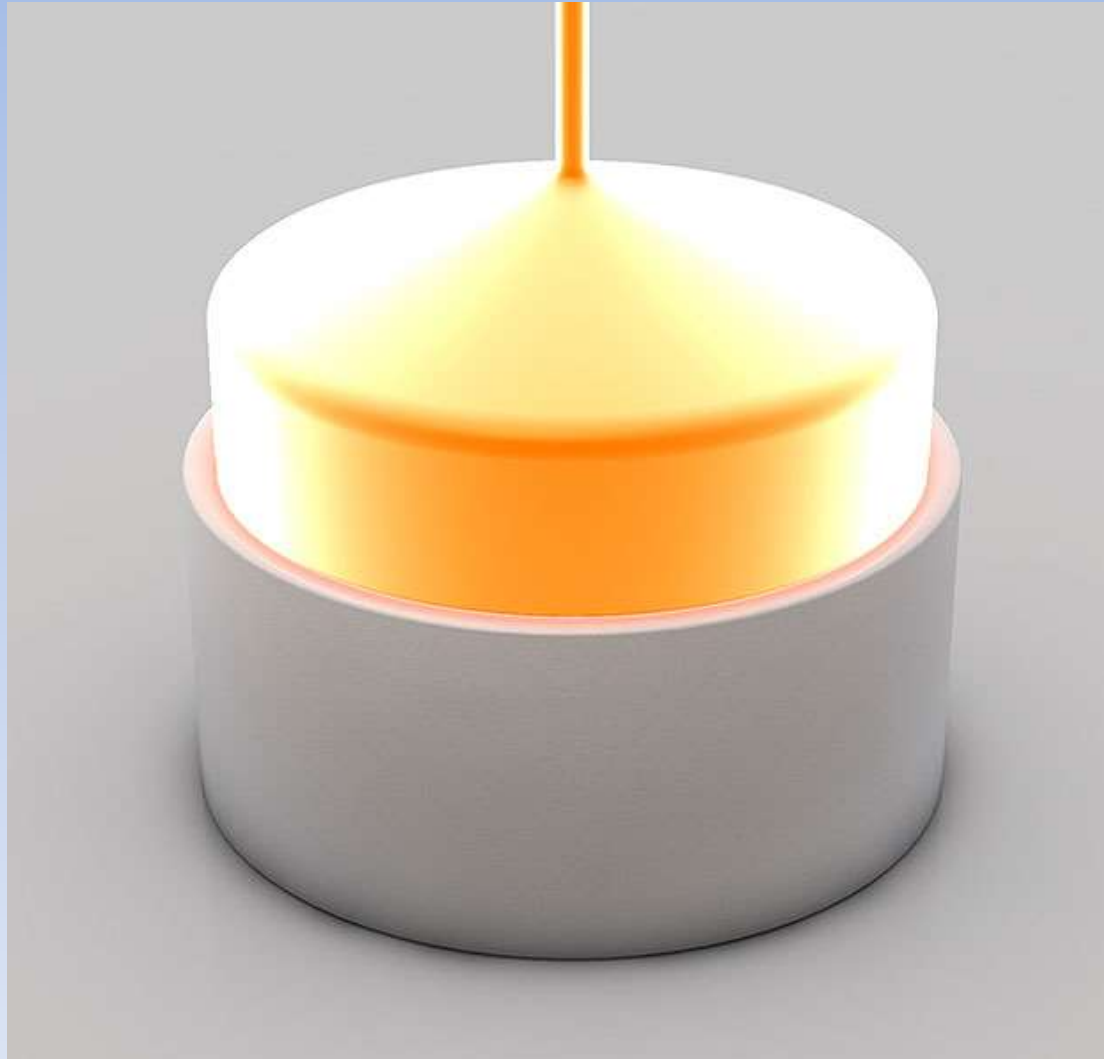
# The Flip-Flop - A Memory Circuit



# Silicon Manufacture



# Silicon Manufacture



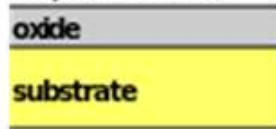


# Silicon Manufacture

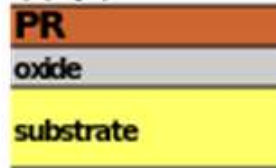


# Integrated Circuit Manufacture

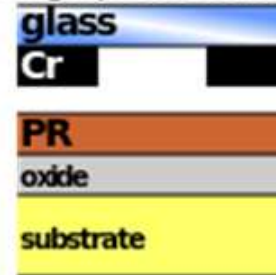
a. Prepare wafer



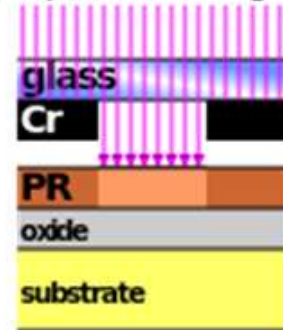
b. Apply photoresist



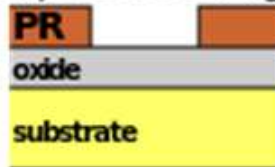
c. Align photomask



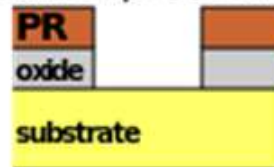
d. Expose to UV light



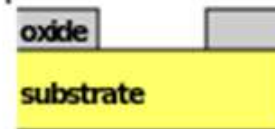
e. Develop and remove photoresist exposed to UV light



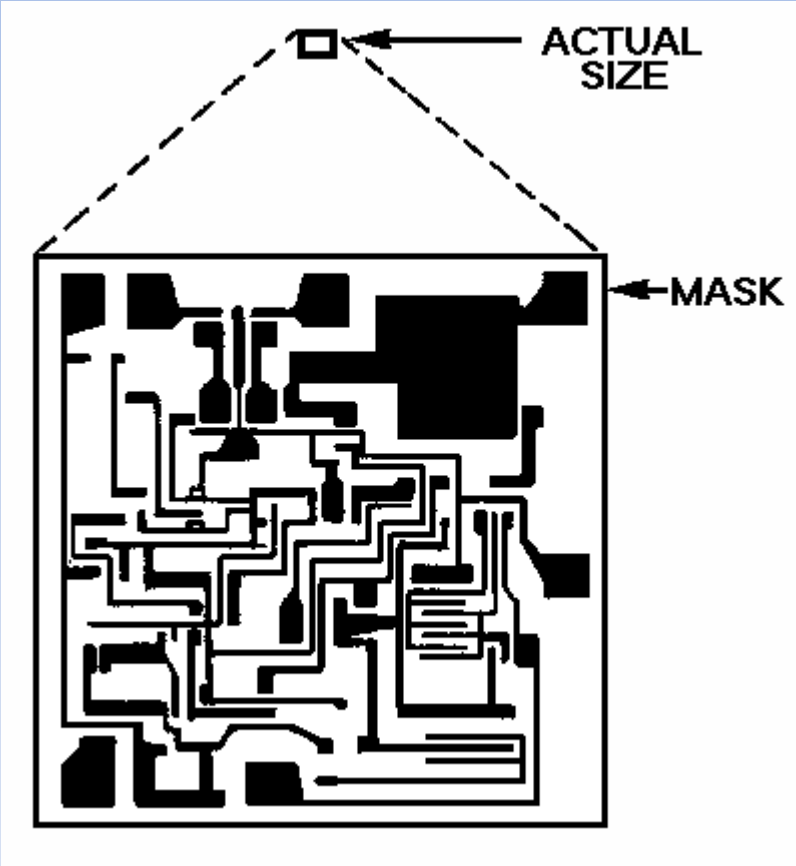
f. Etch exposed oxide



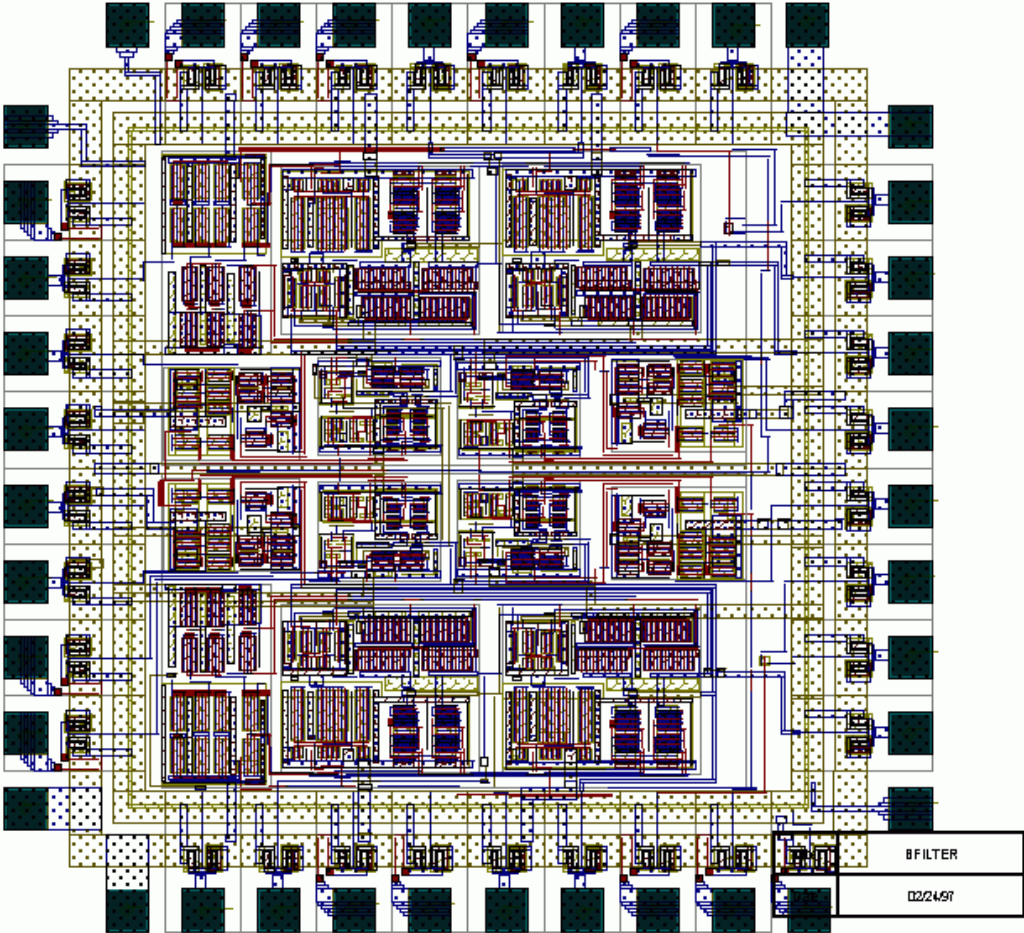
g. Remove remaining photoresist



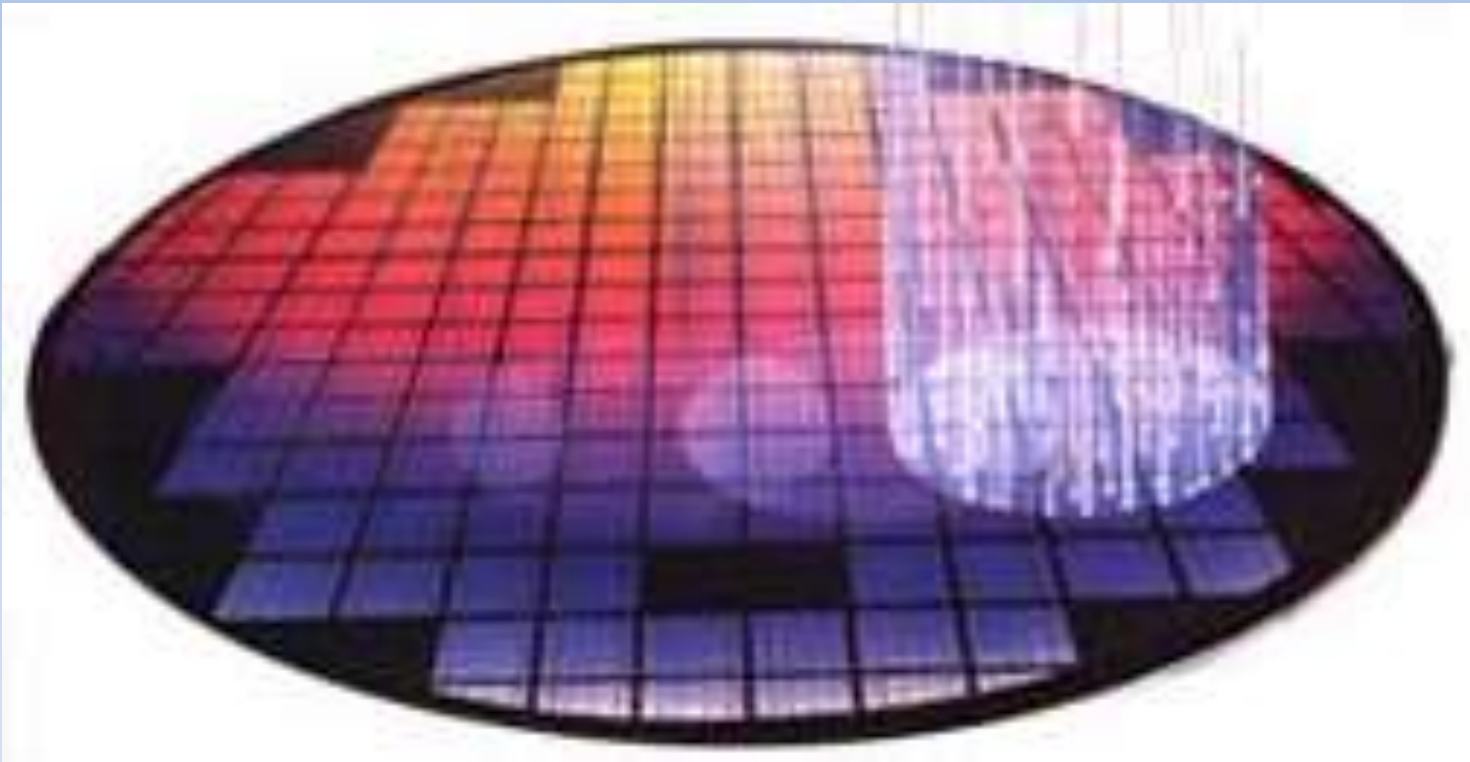
# Integrated Circuit Manufacture - Photographic Masks



# Integrated Circuit Manufacture - Photographic Masks



# Integrated Circuit Manufacture - Ion Implantation



# Chip Components – Smallest Dimensions

<b>1975</b>	<b>1 micron</b>	<b>0.001 mm</b>
<b>2013</b>	<b>20 nanometers</b>	<b>0.00002 mm</b>

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## Silicon Wafer Sizes

<b>1975</b>	<b>2.5 inches diameter</b>
<b>2013</b>	<b>12 inches diameter</b>

**RISC**

**CISC**



**RISC**

**Small**

**CISC**

**Large**

**RISC**

**Small**

**Cheap**

**CISC**

**Large**

**Expensive**

# RISC

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Fast

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# CISC

Large

Expensive

Slow

High Power

Less Reliable

# RISC

Small

Cheap

Fast

Low Power

More Reliable

Runs Cool

# CISC

Large

Expensive

Slow

High Power

Less Reliable

Runs Hot

# RISC

Acorn ARM

4 MHz

1.61 seconds

# CISC

Intel 80486

33 MHz

1.59 seconds